TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	SYSTEM AND METHOD FOR IMPLEMENTING A MICRO-STEPPING DELAY CHAIN FOR A DELAY LOCKED LOOP
Application Number	
Date :	
First Named Applica	nt: Harold Pilo
Confirmation Number:	
Attorney Docket Nur	ber: BUR920030199US1

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Submitted By:	Elec. Sign.	Sign. Capacity
Robert A. Walsh Registered Number: 26,516	Robert A. Walsh /s	Attorney

Documents being submitted:	Files
	BUR920030199US1-usidst.xml
us-ids	us-ids.dtd
	us-ids.xsl
us-assignment	BUR920030199US1-usassn.xml
us-assignment	us-assignment.xsl
	us-assignment.dtd
	BUR920030199US1Assign1.tif
	BUR920030199US1Assign2.tif
us-request	BUR920030199US1-usrequ.xml
us request	us-request.dtd
	us-request.xsl
us-fee-sheet	BUR920030199US1-usfees.xml
•	us-fee-sheet.xsl
	us-fee-sheet.dtd
us-declaration	BUR920030199US1DEC1.tif
us-declaration	BUR920030199US1DEC2.tif
us-declaration	BUR920030199US1DEC3.tif
application-body	BUR920030199US1SPEC-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
	isocyr1.ent
	isocyr2.ent
	isodia.ent
	isolat1.ent
	isolat2.ent
	isonum.ent
	isopub.ent
	mmlextra.ent
	mmlalias.ent
	soextblx.dtd BUR920030199US1Table1.tif
	BUR920030199US1Drawing1.tif
	BUR920030199US1Drawing2.tif BUR920030199US1Drawing3.tif
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BUR920030199US1Drawing4.tif
BUR920030199US1Drawing5.tif
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Comments

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